

**MIXED NOBLE METAL/NOBLE
METAL OXIDE BOTTOM ELECTRODE FOR
ENHANCED PGO C-AXIS NUCLEATION AND GROWTH**

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BACKGROUND OF THE INVENTION

1. Field of the Invention

This invention generally relates to integrated circuit (IC) fabrication and, more particularly, to a ferroelectric capacitor that uses a bottom electrode mixture of a noble metal and noble metal oxide to promote c-axis nucleation and growth of a PGO ferroelectric film.

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2. Description of the Related Art

Pb₅Ge₃O₁₁ (PGO) single-phase thin films exhibit ferroelectric properties when oriented along their c-axis. A c-axis oriented PGO film has desirable memory characteristics that make it suitable for use in one-transistor (1T) memory devices.

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It has been found that PGO thin film grown on an Ir substrate exhibit stronger c-axis orientation than PGO grown on a Pt substrate, under equivalent deposition conditions. By comparing the XRD spectrum of Ir and Pt substrates (see Figs. 1A and 1B), it can be seen that the Pt substrate shows a stronger preference for (111) peaks than Ir, at comparable film thicknesses. Since the crystal structure and lattice constant of Ir and Pt are about the same, 3.839 and 3.923 respectively, and the lattice constant of PGO is a=10.251 and c=10.685, the question arises as to whether the crystalline orientation may be a factor in promoting c-axis PGO nucleation and growth.

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Although Ir bottom electrodes are known to enhance nucleation and the growth of c-axis PGO, Ir has one significant drawback. Ir is difficult to polish. Therefore, it can be a problem to use simple

chemical-mechanical polish (CMP) processes, to form self-aligned gate structures in one-transistor integration, when using Ir as bottom electrode. Typically, an etching process must be used to define the Ir bottom electrode, which can induce etching damage at the gate and source/drain regions. Furthermore, a buffer polysilicon layer and a conductive barrier layer between the bottom electrode and polysilicon gate are usually needed in order to reduce the etching damage to the source drain region. These extra steps make the 1T integration process more complicated. However, Pt is easily polished.

10 It would be advantageous if Pt bottom electrodes could be used in 1T fabrication processes instead of Ir, to simplify the polishing processes.

 It would be advantageous if Pt bottom electrodes could be formed that promote c-axis orientation in an overlying PGO ferroelectric thin film.

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SUMMARY OF THE INVENTION

The present invention describes a polycrystalline Pt-PtOx bottom electrode underlying a PGO ferroelectric film deposition, for use in DRAM and FeRAM applications. The polycrystalline Pt-PtOx bottom electrode promotes a highly oriented c-axis PGO thin film, to simplify non-volatile memory integration processes. This resultant structure can also be used in capacitors, pyroelectric infrared sensors, optical displays, optical switches, piezoelectric transducers, and surface acoustic wave devices.

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Accordingly, a method is provided for forming a single-phase c-axis PGO film overlying a Pt metal electrode. Although the method is summarized in the context of a Pt bottom electrode, it has a broader application to other noble metals, are explained in the Detailed Description, below. The method comprising: forming a bottom electrode mixture of Pt and Pt₃O₄; forming a single-phase c-axis PGO thin film overlying the bottom electrode; and, forming a top electrode overlying the PGO thin film. Forming a bottom electrode mixture of a Pt and Pt₃O₄ includes: forming a Pt first layer; and, forming a second layer, interposed between the first layer and the PGO thin film, of fully oxidized Pt₃O₄. In other aspects, forming a bottom electrode mixture of Pt and Pt₃O₄ includes forming a polycrystalline mixture of Pt and Pt₃O₄.

A single-phase c-axis PGO film capacitor is also provided. Again, a Pt bottom electrode is summarized, but other noble metal bottom electrodes are described in the Detailed Description, below. The capacitor comprises a bottom electrode mixture of Pt and Pt₃O₄; a single-phase c-axis PGO thin film overlying the bottom electrode; and, a top electrode overlying the PGO thin film. The bottom electrode mixture may include: a Pt first layer; and, a second layer, interposed between the first layer and the PGO thin film, of fully oxidized Pt₃O₄. Alternately, the bottom electrode mixture of Pt and Pt₃O₄ may be understood to be a polycrystalline mixture of Pt and Pt₃O₄.

Additional details of the above-described PGO capacitors and PGO fabrication processes are provided below.

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BRIEF DESCRIPTION OF THE DRAWINGS

Figs. 1A and 1B are drawings depicting the x-ray diffraction (XRD) of PGO grown on Ir, Pt, and Pt-PtOx substrates.

Fig. 2 is a partial cross-sectional view of the present invention Pb₅Ge₃O₁₁ (PGO) ferroelectric capacitor.

Fig. 3 is a partial cross-sectional view of the present invention single-phase c-axis PGO film capacitor, with a Pt bottom electrode.

Fig. 4 is a drawing of an XRD spectrum of PGO, deposited on a mixture of Pt and an oxide of Pt, at different annealing conditions.

Fig. 5 is a drawing of an XRD spectrum of Pt-PtOx at different annealing conditions.

Fig. 6 is a drawing depicting the sheet resistance of as-deposited Pt-PtOx annealed at 500°C and 550°C for 15 minutes.

Fig. 7 is a flowchart illustrating the present invention method for forming a single-phase c-axis Pb₅Ge₃O₁₁ (PGO) ferroelectric film overlying a noble metal electrode.

Fig. 8 is a flowchart illustrating the present invention method for forming a single-phase c-axis PGO film overlying a Pt metal electrode.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 2 is a partial cross-sectional view of the present invention Pb₅Ge₃O₁₁ (PGO) ferroelectric capacitor. The capacitor 200 comprises a bottom electrode 202 (BE) including a polycrystalline mixture of a first noble metal and an oxide of the first noble metal. A single-phase

c-axis PGO ferroelectric thin film 204 overlies the bottom electrode 202. A top electrode 206 (TE) overlies the PGO ferroelectric thin film 204. In some aspects, the PGO ferroelectric thin film 204 is a pure c-axis PGO film.

5 The bottom electrode 202 may be a noble metal such as Pt, Ir, or Ru. When the polycrystalline mixture of the first noble metal and first noble metal oxide is either Pt or Ir, the bottom electrode 202 has a preference in the (111) orientation. That is, an XRD spectrum of such a bottom electrode would reveal a primarily (111) orientation, although
10 traces of other orientations may also exist.

 In some aspects, the bottom electrode 202 polycrystalline mixture includes a first layer 208 including a mixture of the first noble metal and first noble metal oxide. A second layer 210 of the first noble metal oxide is interposed between the first layer 208 and the ferroelectric
15 thin film 204. Alternately, the first layer 208 may be a (pure) first noble metal first layer, and the second layer 210 may be a first noble metal oxide. That is, the second layer 210 is the metal oxide of the first noble metal. For example, if the bottom electrode is Pt, then the first layer 208 may be Pt, and the second layer 210 fully oxidized Pt_3O_4 . In some aspects,
20 the bottom electrode 202 has a sheet resistance of less than 5 ohms/square.

 Fig. 3 is a partial cross-sectional view of the present invention single-phase c-axis PGO film capacitor, with a Pt bottom electrode. The capacitor 300 comprises a bottom electrode 302 mixture of
25 Pt and Pt_3O_4 . A single-phase c-axis PGO thin film 304 overlies the bottom electrode 302. A top electrode 306 overlies the PGO thin film 304. In

some aspects, the PGO ferroelectric thin film 304 is a pure c-axis PGO film. In other aspects, the bottom electrode 302 has a sheet resistance of less than 5 ohms/square.

More specifically, the bottom electrode 302 mixture of Pt and Pt₃O₄ includes a (pure) Pt first layer 308 and a second layer 310 of fully oxidized Pt₃O₄ interposed between the first layer 308 and the PGO thin film 304. Note, other aspects of the invention may use a second layer 310 of non-fully oxidized Pt, or a mixture of Pt with a Pt oxide. Alternately, the bottom electrode 302 mixture of Pt and Pt₃O₄ may be understood to a polycrystalline mixture of Pt and Pt₃O₄.

Functional Description

One assumption supporting the present invention is that polycrystalline bottom electrode structures promote the c-axis orientation of an overlying PGO thin film. Conventionally, Ir is closer to a polycrystalline structure than Pt, which is typically an epitaxial or single-crystal structure. Further, from Auger Electron Spectroscopy (AES) data, a thin layer of IrO₂ can be assumed to form over a Ir bottom electrode during conventional PGO deposition and annealing processes. Using conventional processes, it is difficult to form Pt oxide over a Pt substrate when annealed in an O₂ ambient environment.

Figs. 1A and 1B are drawings depicting the x-ray diffraction (XRD) of PGO grown on Ir, Pt, and Pt-PtOx substrates. Fig. 1B is the same spectrum as Fig. 1A, scaled differently, so that Ir, Pt, and Pt-PtOx (mixture of Pt and Pt oxide) can be compared.

Fig. 4 is a drawing of an XRD spectrum of PGO, deposited on a mixture of Pt and an oxide of Pt, at different annealing conditions.

Fig. 5 is a drawing of an XRD spectrum of Pt-PtOx at different annealing conditions.

5 Since pure Pt is difficult to oxidize when annealed in oxygen ambient, conventionally it has been relatively difficult to grow highly oriented single-phase c-axis PGO on pure Pt, which is typically in a strong (111) orientation. In order to modify the surface condition, the present invention Pt is sputter deposited in atmosphere mixture of Ar and O₂,
10 instead of pure Ar.

 Comparing the XRD spectrums of Figs. 1A and 5, it can be seen that the intensity of (111) peaks of a Pt electrode are significantly reduced using the present invention process. As seen in Fig. 5, Pt₃O₄ peaks are detected, indicating that crystal structure of the electrode is
15 modified to a polycrystalline Pt and PtOx mixture.

 PGO thin film is deposited on this Pt-PtOx bottom electrode. Fig. 1B is the XRD spectrum of PGO grown on Pt-PtOx, Pt, and Ir, showing that the c-axis peaks of PGO on Pt-PtOx or Ir are about the same, and that these peaks are much stronger than those associated with
20 the (pure) Pt substrate.

 Since Pt-PtOx can be easily CMP polished, the use of such a material as a bottom electrode simplifies integration processes and reduces the etching damage at the gate and source drain region in FeRAM and DRAM devices, especially in single-transistor applications.

25 The following is an example of a process to deposit Pt-PtOx as a bottom electrode. The Pt-PtOx bottom electrode is deposited by

reactive sputtering a pure Pt target in Ar and O₂ mixture ambient. The base pressure is about 7×10^{-7} Torr. The power on the Pt target is 50 watts (W) to 500 W, for a 4" diameter target. The partial pressure of Ar and O₂ are 1-5 milliTorr (mTorr) and 1-5 mTorr, respectively.

5 After the Pt-PtOx deposition, an oxygen ambient annealing is performed. Specifically, a rapid thermal anneal (RTA) is performed at 400-800° C, for a duration of 1 minute to 1 hour.

 Referencing Fig. 5, the XRD spectra of Pt-PtOx after different annealing conditions, it can be seen that the as-deposited Pt-
10 PtOx is much more amorphous. After being annealed at 500° C for 15 minutes, peaks of polycrystalline Pt₃O₄ are detected. Pt₃O₄ has a lattice constant of $a=5.585$. After being annealed at 550° C for 15 minutes, peaks of the Pt₃O₄ phase weaken, as the Pt peaks remain about the same.

 Fig. 6 is a drawing depicting the sheet resistance of as-
15 deposited Pt-PtOx annealed at 500°C and 550°C for 15 minutes. Although PtOx conventionally exhibits large resistivity, the grain growth of Pt appears to lower the resistivity of Pt-PtOx mixture electrode.

 The PGO thin film can be spin-coated using lead acetate and germanium isopropoxide in a 2-(2-ethoxyethoxy) ethanol solution, heated
20 with air exposure to form a deep red-brown color. The baking temperature is around 50-350° C, for 30 seconds to 3600 seconds. The annealing temperature after each spin-on layer is around 400-550° C, for 30 seconds to 3600 seconds. The final annealing temperature is around 450-600° C, for 5 minutes to 3 hours. Note, the present invention is not
25 limited to any particular PGO deposition process. Other PGO depositions, besides the one mentioned above, are known in the art.

Fig. 7 is a flowchart illustrating the present invention method for forming a single-phase c-axis $\text{Pb}_5\text{Ge}_3\text{O}_{11}$ (PGO) ferroelectric film overlying a noble metal electrode. Although the method is depicted as a sequence of numbered steps for clarity, no order should be inferred from the numbering unless explicitly stated. It should be understood that some of these steps may be skipped, performed in parallel, or performed without the requirement of maintaining a strict order of sequence. The method starts at Step 700.

Step 702 forms a bottom electrode polycrystalline mixture of a first noble metal and an oxide of the first noble metal. In some aspects, the bottom electrode mixture has a sheet resistance of less than 5 ohms/square. Step 704 forms a single-phase c-axis PGO ferroelectric thin film overlying the bottom electrode. In one aspect, Step 704 forms a pure c-axis PGO thin film. Step 706 forms a top electrode overlying the PGO ferroelectric thin film.

In some aspects of the method, forming a bottom electrode polycrystalline mixture of a first noble metal and an oxide of the first noble metal in Step 702 includes using a noble metal such as Pt, Ir, or Ru. When the noble metal is either Pt or Ir, Step 702 may include forming a polycrystalline mixture having a preference in the (111) orientation.

In one aspect, forming a bottom electrode polycrystalline mixture of the first noble metal and first noble metal oxide includes substeps. Step 702a forms a first layer including a mixture of the first noble metal and first noble metal oxide. Step 702b forms a second layer, interposed between the first layer and the ferroelectric thin film, of the first noble metal oxide.

In another aspect, Step 702c forms a first noble metal first layer. Step 702d forms a second layer, interposed between the first layer and the ferroelectric thin film, of the first noble metal oxide. For example, Step 702c may form a Pt first layer. Step 702d forms a second layer,
5 interposed between the first layer and the ferroelectric thin film, of fully oxidized Pt_3O_4 .

In a different aspect, Step 702e reactive sputters the first noble metal in an atmosphere including Ar and O_2 . Step 702f anneals the mixture in an O_2 atmosphere. For example, Step 702e may reactive
10 sputter in an atmosphere including Ar and O_2 includes, with respect to a base pressure of 7×10^{-7} Torr. Further, Step 702e may use an Ar partial pressure in the range of 1 to 5 mTorr and an O_2 partial pressure in the range of 1 to 5 mTorr. A sputtering power in the range of 50 to 500 watts may be used in Step 702e, with respect to a 4-inch diameter target.

15 Annealing the mixture in an O_2 atmosphere (Step 702f) may include rapid thermal annealing (RTA) at a temperature in the range of 400 to 800 degrees C, for a duration in the range of 1 to 60 minutes.

Fig. 8 is a flowchart illustrating the present invention method for forming a single-phase c-axis PGO film overlying a Pt metal
20 electrode. The method starts at Step 800. Step 802 forms a bottom electrode mixture of Pt and Pt_3O_4 . Step 804 forms a single-phase c-axis PGO thin film overlying the bottom electrode. Step 806 forms a top electrode overlying the PGO thin film.

In some aspects, forming a bottom electrode mixture of a Pt
25 and Pt_3O_4 includes substeps. Step 802a forms a Pt first layer. Step 802b

forms a second layer, interposed between the first layer and the PGO thin film, of fully oxidized Pt_3O_4 .

In another aspect, forming a bottom electrode mixture of Pt and Pt_3O_4 in Step 802 includes forming a polycrystalline mixture of Pt and Pt_3O_4 . In a different aspect, Step 802 forms a bottom electrode mixture having a sheet resistance of less than 5 ohms/square.

PGO ferroelectric capacitors formed of noble metal/noble metal oxide mixtures, and related fabrication processes have been presented. Specific processes have been presented to illustrate the invention using Pt as an example. However, the invention is not limited to merely these examples. Ru and Ir have been suggested as other bottom electrode materials, but the invention may have application to an even broader range of noble metals. Further, the present invention process may also have application to other ferroelectric materials besides PGO. Although the presented processes are limited to the fabrication of capacitors, those skilled in the art can appreciate that the invention has application to devices such as 1T memories, and other devices that may use ferroelectric capacitors. These other devices may include pyroelectric infrared sensors, optical displays, optical switches, piezoelectric transducers, and surface acoustic wave devices. Other variations and embodiments of the invention will occur to those skilled in the art.

WE CLAIM:

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